Applications Information Page 11-20 -- Table 11-4
Support Devices & Products lists National Semiconductor Part REPROM
with Burst Capability with an incorrect part number: NM27P6841.
Part number needs to be changed to NM27P210, for a one-meg EPROM with
burst capability, or NM27P040 for a 4 meg EPROM with burst, or
NM27P512 for a 512K EPROM with burst.
PN,7/19/94

**
Exception Processing Page 8-23 .
The SIZE field should read as follows:
00 - LONG
01 - BYTE
10 - WORD
11 - MOVE16 or Double Precision
JG, 8/2/94

**
Exception Processing Page 8-25
The description of the non-recoverable write cases must read as
follows:
      PBE=1, WE=1
      SBE=1, WE=1
      RW=11, IO=0, WE=1, MA=1(Bus error on misaligned read-Modify-write)
      RW=01, WE=1 for a move <ea>,<ea> in which the destination
                  operand writes over the source operand.
JG, 8/2/94

**
Applications Information Fig 11-4 (page 11-10).
Capacitor values C5 and C6 are 0.01 uF and 0.22 uF respectively.
JG, 8/2/94

**
Signal Description section 2.12 (page 2-16)
THERM0 and THERM1 has a temperature coefficient of 2.8 ohms/degC and
the nominal resistance is 780 ohms at 25 degC.
JG, 8/26/94

**
Caches page 5-5, 5-6.
There are a couple of references that describes the effect of a
hardware reset on the caches. They should read: A hardware reset
clears the CACR, disabling both caches; however, a reset does not
invalidate the cache tags. The CINV instruction must invalidate the
caches before enabling them. Operation of the processor is undefined
if the caches are enabled without executing the CINV instruction.
JG 8/26/94

**
Introduction page 1-7
The section 1.4.2 describes the Execution Unit, not Integer Unit. All
references to the Integer Unit in this section is applicable to the
Execution Unit.
JG 9/7/94

** Bus Operation page 7-5

SLE = 1 means shadow locke asserted, =0 means shadow locke negated.
JG 9/22/94

** Electrical Specifications Section 12
The 40MHz version of the EC060 will not be productized. Hence, all
references to the 40MHz EC060 are no longer applicable. There are also other
corrections. See attached faxable document "060tspec.eps.
JG 1/16/95

** Ordering Information and Mechanical Data Section 13
The 40MHz version of the EC060 will not be productized. Hence, all
references to the 40MHz EC060 are no longer applicable. Also, the
208-pin QFP package has been changed to the 240-pin QFP. See attached
faxable document "060cqfp.eps".
JG 1/16/95

This entry contains a faxable illustration of the MC68060 CQFP. To
recieve a copy of it, execute the FAXBACK command from the menu above.

TYPE
faxbak
/aesop/novu/msc/060cqfp.ps
ENDTYPE